



DEVICE PROVIDING ELECTRICAL CONTACT TO THE SURFACE OF A  
SEMICONDUCTOR WORKPIECE DURING METAL PLATING

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## BACKGROUND OF THE INVENTION

This application claims the priority of U.S. provisional  
10 application no. 60/190,023, filed March 17, 2000, the  
disclosure of which is expressly incorporated by reference  
herein.

Multi-level integrated circuit (IC) manufacturing requires  
15 many steps of metal and insulator film depositions followed by  
photoresist patterning and etching or other means of material  
removal. After photolithography and etching, the resulting  
wafer or substrate surface is non-planar and contains many  
features such as vias, lines or channels. Often, these features  
20 need to be filled with a specific material such as a metal or  
other conductor. Once filled with a conductor, the features  
provide the means to electrically interconnect various parts of  
the IC.

25 Electrodeposition is a technique used in IC manufacturing  
for the deposition of a highly conductive material, such as  
copper (Cu), into the features on the semiconductor wafer  
surface. Figure 1 is a schematic illustration of a wafer or  
substrate 16 to be coated with Cu. Features 1 may be vias,  
30 trenches, bond pads, etc., and are opened in the dielectric or

insulator layer 2. To achieve Cu deposition, a barrier layer 3 is first deposited over the whole wafer surface. Then, a conductive Cu seed layer 4 is deposited over the barrier layer 3. An electrical contact is made to the barrier layer 3 and/or the seed layer 4, the wafer surface is exposed to a Cu plating electrolyte, and a cathodic voltage is applied to the wafer surface with respect to an anode which also makes physical contact with the electrolyte. In this way, Cu is plated out of the electrolyte, onto the wafer surface, and into the features

10 1.

The terms "wafer" and "substrate" are used interchangeably above and throughout the remaining description. Referring to the example shown in Figure 1, it is to be understood that the "wafer" or "substrate" referred to includes the wafer WF per se, the dielectric or insulator layer 2, and the barrier layer 3, with or without the seed layer 4. These terms, of course, may also refer to a wafer WF per se, including one or more previously processed layers, a further dielectric or insulator layer, and a further barrier layer, with or without a further seed layer.

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The electrical contact to the seed layer and/or the barrier layer is typically made along the periphery of the wafer, which is usually round. This approach works well for thick and highly conductive seed layers and small wafer

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diameters (e.g. 200mm). However, the trend in the semiconductor industry is to go to larger wafers (e.g. 300mm) and smaller feature sizes (smaller than 0.18 microns). Smaller feature sizes, as well as cost considerations, require the use of the thinnest possible seed layers. As the wafer size increases, the plating current value also increases. As the seed layer thickness decreases, the sheet resistance increases, and the voltage drop between the middle and the edge of a large wafer also increases. Therefore, voltage drop becomes a major problem, especially for large wafers with thin seed layers. This voltage drop results in non-uniform Cu deposition on the wafer surface, the regions near the contacts being typically thicker than other regions.

One other consideration in Cu plating is the "edge exclusion". Cu plating heads, such as the one described in commonly assigned, copending application serial no. 09/472,523, filed December 27, 1999, titled WORK PIECE CARRIER HEAD FOR PLATING AND POLISHING, typically use contacts around peripheries of the wafers. Making electrical contact and, at the same time, providing a seal against possible electrolyte leakage is difficult.

Figure 1a shows a cross sectional view of a contacting scheme in which the wafer or substrate 16 is contacted by a ring-shaped contact 17 which is sealed by a ring seal 18

against exposure to the electrolyte 9a. The seal 18 also prevents the electrolyte 9a from reaching the back surface of the wafer or substrate 16. Such a contacting scheme extends a distance "W" from the edge of the wafer. The distance "W" is referred to as "edge exclusion" and may typically be 3-7mm. Minimizing "W" would allow better utilization of the wafer surface for IC fabrication.

There is, therefore, a need to develop new and novel approaches to provide electrical contacts to the surface of semiconductor wafers during electrodeposition of conductors.

#### SUMMARY OF THE INVENTION

It is a primary object of this invention to provide both a device and a method by which substantially uniform deposition of conductive material on a surface of a substrate, which includes a semiconductor wafer, from an electrolyte containing the conductive material is made possible. According to the invention, a first conductive element can contact or otherwise electrically interconnect with the substrate surface at locations disposed over substantially all of the surface. Upon application of a potential between the first conductive element and a second conductive element, while the electrolyte makes physical contact with the surface and the second conductive element, the conductive material is deposited on the

surface.

In one preferred form of the invention, the first  
conductive element is provided with multiple electrical  
5 contacts. The multiple electrical contacts may include pins  
extending from the first conductive element, rollers biased and  
electrically interconnected, at least in part, by springs with  
the first conductive element, or various combinations of such  
pin and spring biased roller contacts. In this form of the  
10 invention, the first conductive element is a cathode plate,  
and the second conductive element is an anode plate. Each pin  
or spring biased roller contact extends through a hole provided  
in the second conductive element, and an insulator is  
interposed between the pin or the spring biased roller contact  
15 and the second conductive element. The electrical contacts are  
biased into contact or at least into electrical connection with  
the substrate surface. The device also includes a pad disposed  
on the second conductive element by which the substrate surface  
can be polished. At least one of the substrate and the second  
20 conductive element can be moved relative to the other while the  
conductive material is deposited on the surface of the  
substrate. This relative movement may be in the form of  
rotation and/or translation. If pins are used as the  
electrical contacts, each pin may have a rounded tip adapted to  
25 contact the substrate surface.

In another preferred form of the invention, the first conductive element can be a conductive pad through which the electrolyte can flow, and the second conductive element can be an anode plate separated by an insulating spacer from the conductive pad. At least one of the substrate and the pad can be rotated or translated relative to the other while the conductive material is deposited on the surface of the substrate, and in this way the substrate surface can be polished by the pad.

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The device can also be used to provide substantially uniform electro-etching of conductive material deposited on the substrate surface when the polarity of the potential applied is reversed. Moreover, the device can be used simply to provide substantially uniform electro-etching of conductive material on the substrate surface. In this case, a first conductive element can be electrically interconnected with the substrate surface over substantially all of the surface. Upon application of a potential between the first and second conductive elements while an electrolyte makes physical contact with the surface of the substrate and the second conductive element, the conductive material on the surface will be etched.

Other features and advantages of the invention will become apparent from the description which follows.

## BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is an illustration of the known structure of a wafer or substrate to be coated with Cu.

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Figure 1a is a cross sectional side view of a wafer or substrate contacting scheme.

Figure 2 is a schematic illustration of an apparatus in which the present invention may be utilized.

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Figure 3 shows one electrical contact embodiment in a device forming the subject matter of the present invention.

Figure 4 shows another electrical contact embodiment.

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Figure 5 is a cross sectional side view similar to Figure 1a but showing a reduction in wafer edge exclusion made possible by the invention.

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Figures 6a, 6b, and 6c show various individual electrical contact distributions.

Figure 7 shows another electrical contact embodiment.

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Figure 8 shows a further electrical contact embodiment.



Figure 9 shows still another electrical contact embodiment.

Figure 10 shows one more electrical contact embodiment.

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Figure 11 is a schematic illustration of a single electrical contact such as that shown in Figure 10 while in contact with a wafer surface during application of an electric field.

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Figure 12 shows part of another electrical contact embodiment which is similar to those of Figures 9 and 10 but in which a roller and a roller support member have different sizes.

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#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

The following is a description of novel approaches to make distributed multiple electrical contact to the wafer surface, all over the surface, rather than just at the periphery. Various approaches are described.

A general depiction of one version of a plating apparatus is shown in Figure 2. This apparatus can also be used for plating and polishing as disclosed in commonly assigned application serial no. 09/201,929, filed December 1, 1998,

titled METHOD AND APPARATUS FOR ELECTROCHEMICAL MECHANICAL DEPOSITION, and commonly assigned, copending application serial no. 09/472,523, filed December 27, 1999, titled WORK PIECE CARRIER HEAD FOR PLATING AND POLISHING. The carrier head 10  
5 holds the wafer 16. The wafer has the barrier layer and the seed layer (not shown in Figure 2) deposited on its surface, and therefore its surface is conductive. The head can be rotated around a first axis 10b. It can also be moved in the x, y, and z directions. A pad 8 is placed on an anode plate 9  
10 across from the wafer surface. The pad surface may itself be abrasive, or the pad may contain an abrasive material. Pad designs and structures form the subject matter of commonly assigned, copending application serial no. 09/511,278, filed February 23, 2000, titled PAD DESIGNS AND STRUCTURES FOR A  
15 VERSATILE MATERIALS PROCESSING APPARATUS, and commonly assigned, copending application serial no. 09/621,969, filed July 21, 2000, titled PAD DESIGNS AND STRUCTURES WITH IMPROVED FLUID DISTRIBUTION.

20 Electrolyte 9a is supplied to the wafer surface through the openings in the anode plate and the pad as shown by the arrows in Figure 2. Commonly assigned, copending application serial no. 09/568,584, filed May 11, 2000, titled ANODE ASSEMBLY FOR PLATING AND PLANARIZING A CONDUCTIVE LAYER,  
25 discloses an anode plate, while commonly assigned, copending application serial no. 09/544,558, filed April 6, 2000, titled

MODIFIED PLATING SOLUTION FOR PLATING AND PLANARIZATION, the disclosure of which is incorporated by reference herein as non-essential material, discloses an electrolyte. The electrolyte then flows over the edges of the pad into the chamber 9c to be  
5 re-circulated after cleaning/filtering/refurbishing. An electrical contact 9d is provided to the anode plate. The anode plate turns around the axis 10c. In some applications, the plate may also be translated in the x, y, and/or z directions. Axes 10b and 10c are substantially parallel to each other. The  
10 diameter of the pad 8 is typically smaller than the diameter of the wafer surface exposed to the pad surface, although it may also be larger. The gap between the wafer surface and the pad is adjustable by moving the carrier head and/or the anode plate in the z direction. In one mode of operation, the workpiece  
15 (i.e., the wafer or substrate) may be brought close to the pad, without touching the pad. In this mode, during material deposition, the workpiece hydroplanes or floats over the pad or anode. In another mode of operation, the wafer surface and the pad may be in contact. When the wafer surface and the pad are  
20 touching, the pressure that is exerted on the wafer and pad surfaces can also be adjusted.

According to a first embodiment of the invention, electrical connection to the wafer surface is made by way of  
25 multiple electrical contacts formed by pins that come up through the pad 8 and touch the wafer surface. Assuming by way

of example that it is the structure shown in Figure 1 that is to be plated, and referring now to Figure 3, it will be understood that the wafer surface 22 is formed by the exposed surface of the seed layer 4. A magnified view of one of the multiple electrical contacts is shown in Figure 3. Holes 24 have been provided in the anode plate 9 to accommodate the pins 20. These pins 20 are electrically isolated from the anode plate 9 by an insulator 26. The insulator may be a ceramic or other appropriate dielectric material. A seal 25 is interposed between the anode plate 9 and the insulator 26. The pins 20 forming the electrical contacts are an integral part of a cathode plate 30, which is also electrically isolated from the anode plate 9 by the insulator 26. The cathode plate 30 is spring loaded by suitable springs 32 which bias or push the rounded tips 20T of the pins 20 towards the wafer surface 22 during the plating operation. Thus, the electrical contacts can slide up under the spring bias and down against the spring bias to adjust dynamically to the carrier head or workpiece location relative to the anode plate.

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A roller ball, similar to that which could be used in a ball-point pen, can be incorporated at the tips 20T to prevent scratching the wafer surface. Various additional or alternative electrical contact configurations will be described in connection with Figures 7-12. Soft conductive brushes can also be used to make contact to the wafer surface. It is

important that the selected contacts do not scratch the wafer surface excessively.

For plating, the electrolyte 9a is supplied to the gap 34  
5 between the pad 8 and the wafer surface 22 and thus is brought  
into physical contact with the wafer surface and the anode  
plate. In one mode of operation, the wafer 16 is brought down  
until its surface 22 makes physical contact to the tips 20T of  
the pins 20. A potential is applied between the cathode plate  
10 30 and the anode plate 9, making the cathode plate 30 more  
negative than the anode plate 9. Therefore, the wafer surface  
is also rendered cathodic through the pins 20. Under applied  
potential, copper plates out of the electrolyte 9a onto the  
wafer surface 22. By adjusting the gap 34 between the pad 8 and  
15 the wafer surface 22 and/or by adjusting the pressure with  
which the pad 8 and the wafer surface 22 touch each other, one  
can achieve just plating, or plating and polishing. For  
effective polishing it is preferred that the pad 8 have an  
abrasive surface or that the whole pad 8 is abrasive.

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During plating, the wafer or substrate 16 and the anode  
plate/pad assembly 8, 9 should rotate with respect to one  
another so that plating takes place uniformly. They may also  
translate in one or two directions. The electrolyte 9a  
25 typically fills any gap 34 between the pad 8 and the wafer  
surface 22. It is most preferable that the electrolyte 9a be

applied through channels in the anode plate 9 and the pad 8 (not shown in Figure 3). Alternately, if the gap 34 is large (e.g. 2mm or larger), the electrolyte can be provided into the gap 34 from the edges of the wafer.

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In other applications, the pin tips 20T, or the tips of other types of electrical contacts which will be described, may be disposed in close proximity to the wafer surface 22 without touching this surface. Moreover, under a potential applied  
10 between the wafer and the anode plate, copper may be either plated onto or removed from the wafer, depending on the polarity of the wafer. Circuitry used for application and adjustment of the applied potential, and for inverting the polarity of the potential, is well known and commonly used.

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In the construction shown in Figure 4, electrical contact to the wafer surface is made by way of a potential conductive pad 80. This pad 80 is used in place of the multiple pins 20. In this case, an insulating spacer 82 of ceramic or other  
20 dielectric material is placed directly over the anode plate 9' between the anode plate 9' and the conductive pad 80. Electrical supply contacts are made to the conductive pad 80 and the anode plate 9', and a cathodic potential is applied to the pad 80, with electrolyte 9a making physical contact to the  
25 anode plate 9', the pad 80 and the wafer surface 22. When the substrate or wafer 16 is brought down and engages the pad, it

gets energized and Cu plating on the wafer surface 22 commences. The construction shown in Figure 4 is similar to certain pad designs and structures forming the subject matter of application serial no. 09/511,278, filed February 23, 2000, 5 mentioned previously. Additionally, commonly assigned application serial no. 09/483,095, filed January 14, 2000, titled SEMICONDUCTOR WORKPIECE PROXIMITY PLATING METHODS AND APPARATUS, discloses conductive pad strips used on cylindrical anodes. In other applications, the potential conductive pad 80 10 may be allowed to float with respect to the wafer surface 22 during material deposition or removal. The potential, moreover, may be pulsed to produce impulse plating. Again, the circuitry used for pulsing the potential is well known and commonly used.

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In both approaches described above and in others which will be described, some Cu plating may take place on the exposed cathodic surfaces besides the wafer surface. In the case of pins, for example, exposed regions of the pins may get 20 coated. In case of a conductive pad, the whole pad may get coated. Therefore, it is of utmost importance to select the right conductive materials to be used for the construction of the electrical contacts and the pads. The materials should be such that plating on the Cu coated wafer surface (i.e. the seed 25 layer 4 of Figure 1) should be preferable or more efficient than plating on the pad or contact surface. Examples of proper

materials for the pads may be various conductive polymers or polymeric materials that are coated with refractory metals such as Ta, alpha Ta, W, Mo or their nitrides. The pins or other electrical contacts can be made of conductive polymers or refractory metals such as Mo, Ta and W; alternatively, the pins or other cathode contacts can be made of any conductive metal such as Cu or Ni, or of a conductive alloy such as Cu-Be, Cu-Ag, Ag-Pt, etc., but these metals or alloys may be coated by a refractory metal or compound and/or a nitride of a refractory metal, such as TaN or TiN, or of a refractory compound. These are just some examples. There are many more materials on which Cu does not deposit efficiently.

By employing this invention, the "edge exclusion" discussed earlier in connection with Figure 1a can be reduced on the wafer. As shown in Figure 5, eliminating the need for a contact ring to contact the periphery of the wafer permits a reduction of the edge exclusion "d". The seal 18 can be either on the surface 22 of the wafer 16 facing the electrolyte 9a or right at the edge 16a of the wafer. The seal 18 may even be disposed on the surface 35 of the wafer 16 facing away from the electrolyte 9a.

Various electrical contact distributions may be used. Figures 6a-6c schematically show three possible types of distribution of pins 20 over a cathode plate 30. As a rule, as



the number of electrical contacts increases, the voltage drop from the center to the edge of the wafer will become smaller, and the thickness of the plated metal becomes more uniform.

5        Thus far, the invention has been described using Cu as the plated metal. However, practically any metal or conductive alloy can be plated on a wafer/substrate surface using this invention.

10        Although the invention has been described with reference to an electroplating technique and apparatus, it is also directly applicable to electroetching and/or electro-polishing techniques and apparatus. In this case, the polarity of the voltage applied between the anode and cathode plates is  
15 reversed, making the substrate surface more positive. An electro-etching electrolyte may be used. Again, the circuitry used for application and adjustment of the voltage, and for inversion of the voltage polarity, is well known and commonly used.

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Figure 7 shows one of a plurality of electrical contacts which may be used as alternatives to, or together with, pins such as the pins 20, or together with other electrical contact configurations, to provide the necessary electrical connection  
25 to a wafer surface. Figures 8-10 and 12 also show additional electrical contact configurations which can be used as

alternatives to, or together with, other contact configurations. Each electrical contact of Figure 7 includes a conductive roller 120, which is preferably spherical in geometry. Rollers having other suitable geometrical shapes, 5 such as cylindrical rollers, may be used. The rollers are preferably coated with a corrosion resistant material such as gold, platinum, palladium, their alloys, or some other appropriate contact alloy material.

10 The roller 120 may be housed in an arrangement that may include, but is not limited to, a contact spring 122 to supply electrical power from the cathode plate (not shown) to the roller 120. The end of the spring 122 also acts as a bearing surface. The spring 122 allows for a gentle but dynamic 15 loading of the roller 120 on the surface of the workpiece. Each spring 122 biases its respective roller toward the wafer surface. In the embodiment shown in Figure 7, the electrical contact per se is formed by the roller 120 and the spring 122 which supports the roller. Each spring 122 extends between the 20 cathode plate (not shown in Figure 7), on which the spring is supported in any appropriate fashion, and the roller 120 supported by the spring. Both the spring 122 and the roller 120 are surrounded by an insulator 124 of a ceramic or other appropriate dielectric material that isolates the spring 122 25 and the roller 120 from an electric field during the process of plating Cu out of the electrolyte. The insulator 124 may be

configured similarly to the insulator 26, represented in Figure 1, but can include a shaped tip 128. The shaped tip 128 and a seal 126 are disposed around the roller 120. The seal 126 may be adhesively or otherwise secured to the inner surface of the  
5 shaped tip.

The seal arrangement is such that the roller 120 rotates freely with respect to the seal 126. The electrolyte fluid boundary layer, and, if the electrolyte forming the subject  
10 matter of copending application serial no. 09/544,558 mentioned above is used, especially the additive in the electrolyte, helps lubricate the roller surface. In addition to housing the roller 120 and the seal 126, the tip 128 also prevents the roller 120 from exposure to the electric field. Figure 11,  
15 which shows one electrical contact according to another embodiment in use, indicates an applied electric field by reference characters E. Consequently, the tip and seal configuration helps prevent or minimize material deposition on the roller 120.

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Figure 8 shows another embodiment in which a rolling pad 230 of conducting material (e.g. metal), preferably with a partially spherical shaped surface, is disposed between the contact spring 222 and a spherical roller 220. The roller 220  
25 rests on the shaped rolling pad 230. The shaped tip 228 and the seal 226 cooperate with the spring biased rolling pad 230

to confine the roller 220 while allowing it to rotate freely along any direction. In a manner similar to the roller 120 of Figure 7, the roller 220 protrudes partly through but is restrained by the perimeter of an end opening in the insulator 5 224 which surrounds the seal 226, the spring 222, and the rolling pad 230. In the embodiment shown in Figure 8, therefore, the electrical contact per se is formed by the roller 220, the spring 222, and the spring biased rolling pad 230 disposed between the roller and the spring.

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Figure 9 shows that a conductive roller 320 may rest on a support member 330 having, for example, a spherical supporting surface rather than on a rolling pad. Multiple support members could be used beneath the roller 320. Such an arrangement is 15 ideal for self-aligned roller contact. In the embodiment shown in Figure 9, the electrical contact per se is formed by the roller 320, the spring 322, and the support member 330.

Besides the advantage of self alignment, the rolling 20 fiction between the roller 320 and the substrate or workpiece is greatly reduced, especially when the workpiece rotates or translates during the process of plating Cu out of the electrolyte. The reduced friction minimizes undesirable workpiece scratching and damage as well as particulate 25 generation.

Other suitable support member geometries could also be used. For instance the cross section of the support member may be triangular, or the roller support may rest on the knife edge of a support member. In another embodiment, shown in Figure 5 10, a spherical support 430 is disposed between a conductive roller pad 432 and the roller 420. In Figure 10, the electrical contact per se is formed by the roller 420, the conductive spring 422, the conductive spherical support 430, and the spring biased and conductive rolling pad 432. Figure 10 11 shows the embodiment of Figure 10 in use during conductive material deposition. Also, as shown in Figure 12, the size of the roller 520 may be different from that of the roller support member 530.

15 It is important that the roller material, the contact spring material, and the like do not degrade or dissolve in the electrolyte of interest. It is also desirable that these materials do not degrade the quality of the material deposited. The roller, for example, must not excessively scratch the 20 deposited film or generate very undesirable particulates. Numerous face contacts may be made around the periphery of the wafer. The individual contacts may be discrete and range from 4 to about 2000 in number, depending on size of the substrate. As the size of the wafer or substrate increases, the number of 25 electrical contacts used should also increase. The roller contacts could also be a continuous race track or a track which

is split into several elements. For example, the periphery may be divided into quadrants or octets. Each quadrant, etc., may contain many more or less uniformly dispersed roller contacts or contact tips.

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Finally, although the invention is described with reference to an electroplating technique and apparatus, it is directly applicable to an electro-etching or electro-polishing technique or apparatus. In this case, the polarity of the voltage applied between the anode and cathode plates is reversed, making the substrate surface more positive. A special electro-etching electrolyte also could be used.

The foregoing disclosure has been set forth merely to illustrate the invention and is not intended to be limiting. Since modifications of the disclosed embodiments incorporating the spirit and substance of the invention may occur to persons skilled in the art, the invention should be construed to include everything within the scope of the appended claims and equivalents thereof.

## WE CLAIM:

1. A device by which substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, can be provided from an electrolyte containing the conductive material comprising:

a first conductive element having multiple electrical contacts which can electrically interconnect with the surface at locations disposed over substantially all of said surface,  
10 and

a second conductive element,

wherein, upon application of a potential between the first and second conductive elements while the electrolyte makes physical contact with the surface and the second conductive  
15 element, the conductive material is deposited on the surface.

2. A device according to claim 1, wherein said multiple electrical contacts include pins extending from said first conductive element.

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3. A device according to claim 1, wherein said first conductive element is a cathode plate and said second conductive element is an anode plate.

25 4. A device according to claim 2, wherein each pin extends through a hole provided in said second conductive

element.

5        5. A device according to claim 4, and further comprising  
an insulator interposed between the pin and said second  
conductive element.

6. A device according to claim 1, wherein said multiple  
electrical contacts are brought into contact with the surface.

10       7. A device according to claim 1, and further comprising  
a pad disposed on said second conductive element by which the  
surface can be polished or otherwise mechanically agitated.

15       8. A device according to claim 1, wherein at least one of  
the substrate and the second conductive element is moved  
relative to the other while the conductive material is  
deposited on the surface.

20       9. A device according to claim 8, wherein said at least  
one of the substrate and the second conductive element is  
rotated while the conductive material is deposited.

25       10. A device according to claim 8, wherein said at least  
one of the substrate and the second conductive element is  
translated while the conductive material is deposited on the  
surface.



11. A device according to claim 2, wherein each pin has a rounded tip adapted to contact said surface.

12. A device according to claim 8, wherein the multiple  
5 electrical contacts include electrical contacts with rollers at tips thereof adapted to contact said surface.

13. A device according to claim 12, wherein said electrical contacts with rollers at tips thereof include  
10 springs biasing the rollers toward said surface.

14. A method of providing substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, which is to be plated with  
15 said conductive material comprising:

providing a first conductive element having multiple electrical contacts which can electrically interconnect with the surface at locations disposed over substantially all of said surface,

20 providing a second conductive element,

physically contacting the surface and the second conductive element with an electrolyte containing the conductive material, and

applying a potential between the first and second  
25 conductive elements so as to deposit the conductive material on said surface from the electrolyte.

15. A method according to claim 14, and further comprising biasing said multiple electrical contacts into contact with the surface.

5 16. A method according to claim 14, and further comprising polishing the surface with a pad disposed on said second conductive element.

17. A method according to claim 16, wherein said  
10 polishing is performed while the conductive material is deposited on the surface.

18. A method according to claim 14, and further comprising moving at least one of the substrate and the second  
15 conductive element relative to the other while the conductive material is deposited on the surface.

19. A method according to claim 18, wherein said at least one of the substrate and the second conductive element is  
20 rotated while the conductive material is deposited.

20. A method according to claim 18, wherein said at least one of the substrate and the second conductive element is translated while the conductive material is deposited.

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21. A method according to claim 14, and further

comprising insulating the multiple electrical contacts from said second conductive element.

22. A method according to claim 14, wherein the multiple  
5 electrical contacts include pins extending from the first conductive element.

23. A method according to claim 14, wherein the multiple electrical contacts include electrical contacts having rollers  
10 at tips thereof biased toward the surface.

24. A method according to claim 14, and further comprising reversing a polarity of said potential so as to electro-etch deposited conductive material.

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25. A device by which substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, can be provided from an electrolyte containing the conductive material comprising:

20 a first conductive element which can electrically interconnect with the surface at locations disposed over substantially all of said surface, and

a second conductive element,

wherein, upon application of a potential between the first  
25 and second conductive elements while the electrolyte makes physical contact with the surface and the second conductive

element, the conductive material is deposited on the surface.

26. A device according to claim 25, wherein said first  
conductive element is a conductive pad through which the  
5 electrolyte can flow.

27. A device according to claim 25, wherein said first  
conductive element is a conductive pad through which the  
electrolyte can flow and said second conductive element is an  
10 anode plate separated by an insulating spacer from the  
conductive pad.

28. A device according to claim 27, wherein the surface  
can be polished or otherwise mechanically agitated by said  
15 conductive pad.

29. A device according to claim 25, wherein at least one  
of the substrate and the first conductive element is moved  
relative to the other while the conductive material is  
20 deposited on the surface.

30. A device according to claim 29, wherein said at least  
one of the substrate and the first conductive element is  
rotated while the conductive material is deposited.

25

31. A device according to claim 29, wherein said at least

one of the substrate and the first conductive element is translated while the conductive material is deposited on the surface.

5        32.     A method of providing substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, which is to be plated with said conductive material comprising:

      providing a first conductive element which can  
10 electrically interconnect with the surface at locations disposed over substantially all of said surface,

      providing a second conductive element,

      physically contacting the surface and the second conductive element with an electrolyte containing the  
15 conductive material, and

      applying a potential between the first and second conductive elements so as to deposit the conductive material on said surface from the electrolyte.

20        33.     A method according to claim 32, wherein said first conductive element is a conductive pad through which the electrolyte can flow.

      34.     A method according to claim 33, and further  
25 comprising polishing the surface with said conductive pad.

35. A method according to claim 34, wherein said polishing is performed while the conductive material is deposited on the surface.

5 36. A method according to claim 33, and further comprising moving at least one of the substrate and the conductive pad relative to the other while the conductive material is deposited on the surface.

10 37. A method according to claim 36, wherein said at least one of the substrate and the conductive pad is rotated while the conductive material is deposited.

38. A method according to claim 36, wherein said at least  
15 one of the substrate and the second conductive pad is translated while the conductive material is deposited on the surface.

39. A method according to claim 32, and further  
20 comprising reversing a polarity of said potential so as to electro-etch deposited conductive material.

40. A device by which substantially uniform electro-  
etching of conductive material on surface of a substrate,  
25 including a semiconductor wafer, can be provided comprising:  
a first conductive element which can contact the surface

over substantially all of said surface, and

a second conductive element,

wherein, upon application of a potential between the first  
and second conductive elements while an electrolyte makes  
5 physical contact with the surface and the second conductive  
element, the conductive material on the surface is etched.

41. A method of providing substantially uniform electro-  
etching of conductive material on a surface of a substrate,  
10 including a semiconductor wafer, comprising:

providing a first conductive element which can contact the  
surface over substantially all of said surface,

providing a second conductive element,

physically contacting the surface and the second  
15 conductive element with an electrolyte, and

applying a potential between the first and second  
conductive elements so as to etch the conductive material on  
said surface.

**AMENDED CLAIMS**

[received by the International Bureau on 03 August 2001 (03.08.01);  
original claims 1, 9, 10, 12, 14, 19, 20, 25, 30-32, 37, 38, 40 and 41 amended;  
original claims 8, 18, 29 and 36 cancelled; remaining claims unchanged (9 pages)]

1. A device by which substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, can be provided from an electrolyte containing the conductive material comprising:

a first conductive element having multiple electrical contacts which can electrically interconnect with the surface at locations disposed over substantially all of said surface, and

a second conductive element,

wherein, upon application of a potential between the first and second conductive elements while the electrolyte makes physical contact with the surface and the second conductive element and while the surface of the substrate and the electrical contacts are moved relative to each other, the conductive material is deposited on the surface.

2. A device according to claim 1, wherein said multiple electrical contacts include pins extending from said first conductive element.

3. A device according to claim 1, wherein said first conductive element is a cathode plate and said second conductive element is an anode plate.



4. A device according to claim 2, wherein each pin extends through a hole provided in said second conductive element.

5. A device according to claim 4, and further comprising an insulator interposed between the pin and said second conductive element.

6. A device according to claim 1, wherein said multiple electrical contacts are brought into contact with the surface.

7. A device according to claim 1, and further comprising a pad disposed on said second conductive element by which the surface can be polished or otherwise mechanically agitated.

9. A device according to claim 1, wherein said at least one of the substrate and the electrical contacts is rotated while the conductive material is deposited.

10. A device according to claim 1, wherein said at least one of the substrate and the electrical contacts is translated while the conductive material is deposited on the surface.

11. A device according to claim 2, wherein each pin has a

rounded tip adapted to contact said surface.

12. A device according to claim 1, wherein the multiple electrical contacts include electrical contacts with rollers at tips thereof adapted to contact said surface.

13. A device according to claim 12, wherein said electrical contacts with rollers at tips thereof include springs biasing the rollers toward said surface.

14. A method of providing substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, which is to be plated with said conductive material comprising:

providing a first conductive element having multiple electrical contacts which can electrically interconnect with the surface at locations disposed over substantially all of said surface,

providing a second conductive element,

physically contacting the surface and the second conductive element with an electrolyte containing the conductive material, and

applying a potential between the first and second conductive elements so as to deposit the conductive material on said surface

from the electrolyte while the surface of the substrate and the electrical contacts are moved relative to each other.

15. A method according to claim 14, and further comprising biasing said multiple electrical contacts into contact with the surface.

16. A method according to claim 14, and further comprising polishing the surface with a pad disposed on said second conductive element.

17. A method according to claim 16, wherein said polishing is performed while the conductive material is deposited on the surface.

19. A method according to claim 14, wherein said at least one of the substrate and the electrical contacts is rotated while the conductive material is deposited.

20. A method according to claim 14, wherein said at least one of the substrate and the electrical contacts is translated while the conductive material is deposited.

21. A method according to claim 14, and further comprising insulating the multiple electrical contacts from said second conductive element.

22. A method according to claim 14, wherein the multiple electrical contacts include pins extending from the first conductive element.

23. A method according to claim 14, wherein the multiple electrical contacts include electrical contacts having rollers at tips thereof biased toward the surface.

24. A method according to claim 14, and further comprising reversing a polarity of said potential so as to electro-etch deposited conductive material.

25. A device by which substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, can be provided from an electrolyte containing the conductive material comprising:

a first conductive element which can electrically interconnect with the surface at locations disposed over substantially all of said surface, and

a second conductive element,

wherein, upon application of a potential between the first and second conductive elements while the electrolyte makes physical contact with the surface and the second conductive element and while the surface of the substrate and the conductive element are moved relative to each other, the conductive material is deposited on the surface.

26. A device according to claim 25, wherein said first conductive element is a conductive pad through which the electrolyte can flow.

27. A device according to claim 25, wherein said first conductive element is a conductive pad through which the electrolyte can flow and said second conductive element is an anode plate separated by an insulating spacer from the conductive pad.

28. A device according to claim 27, wherein the surface can be polished or otherwise mechanically agitated by said conductive pad.

30. A device according to claim 25, wherein said at least one of the substrate and the first conductive element is rotated while

the conductive material is deposited.

31. A device according to claim 25, wherein said at least one of the substrate and the first conductive element is translated while the conductive material is deposited on the surface.

32. A method of providing substantially uniform deposition of conductive material on a surface of a substrate, including a semiconductor wafer, which is to be plated with said conductive material comprising:

providing a first conductive element which can electrically interconnect with the surface at locations disposed over substantially all of said surface,

providing a second conductive element,

physically contacting the surface and the second conductive element with an electrolyte containing the conductive material, and

applying a potential between the first and second conductive elements so as to deposit the conductive material on said surface from the electrolyte while the surface of the substrate and the first conductive element are moved relative to each other.

33. A method according to claim 32, wherein said first conductive element is a conductive pad through which the

electrolyte can flow.

34. A method according to claim 33, and further comprising polishing the surface with said conductive pad.

35. A method according to claim 34, wherein said polishing is performed while the conductive material is deposited on the surface.

37. A method according to claim 33, wherein said at least one of the substrate and the conductive pad is rotated while the conductive material is deposited.

38. A method according to claim 33, wherein said at least one of the substrate and the second conductive pad is translated while the conductive material is deposited on the surface.

39. A method according to claim 32, and further comprising reversing a polarity of said potential so as to electro-etch deposited conductive material.

40. A device by which substantially uniform electro-etching of conductive material on surface of a substrate, including a

semiconductor wafer, can be provided comprising:

a first conductive element which can contact the surface over substantially all of said surface, and

a second conductive element,

wherein, upon application of a potential between the first and second conductive elements while an electrolyte makes physical contact with the surface and the second conductive element and while the surface of the substrate and the first conductive element are moved relative to each other, the conductive material on the surface is etched.

41. A method of providing substantially uniform electro-etching of conductive material on a surface of a substrate, including a semiconductor wafer, comprising:

providing a first conductive element which can contact the surface over substantially all of said surface,

providing a second conductive element,

physically contacting the surface and the second conductive element with an electrolyte, and

applying a potential between the first and second conductive elements so as to etch the conductive material on said surface while the surface of the substrate and the first conductive element are moved relative to each other.



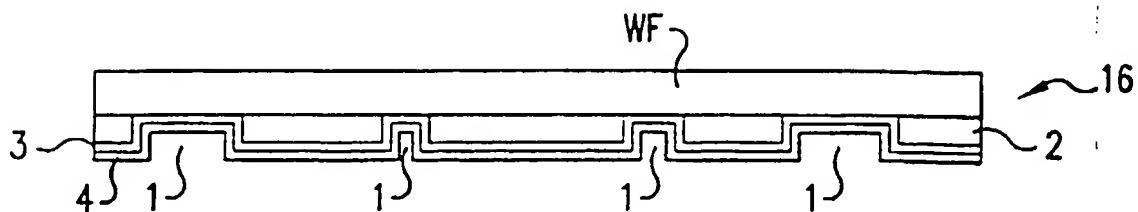


FIG. 1  
PRIOR ART

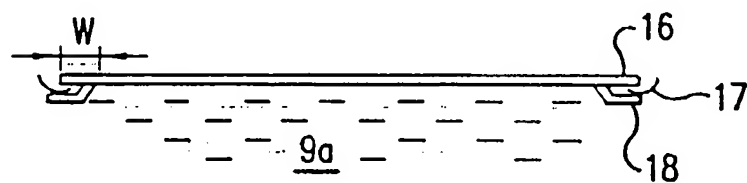


FIG. 1A

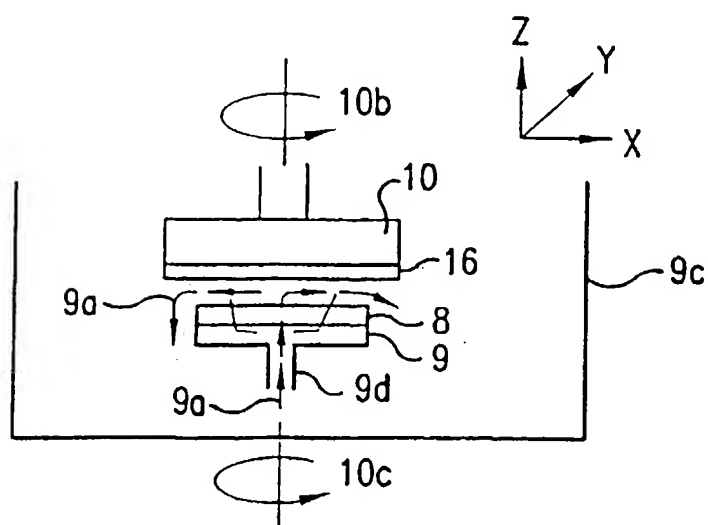


FIG. 2

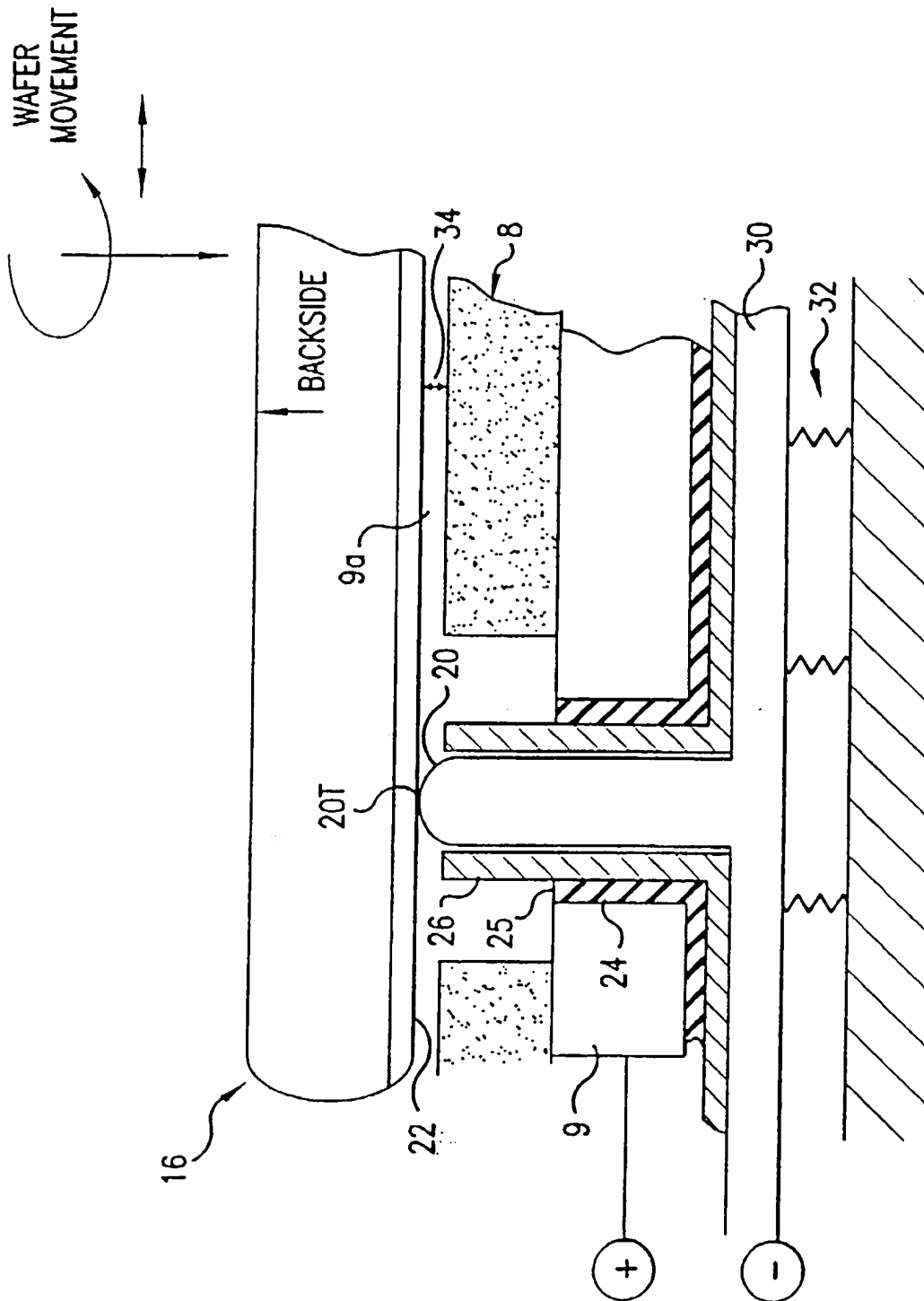


FIG. 3

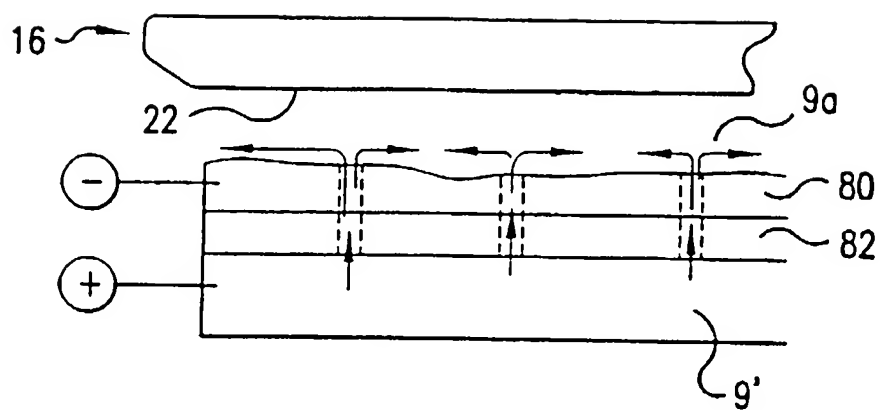


FIG.4

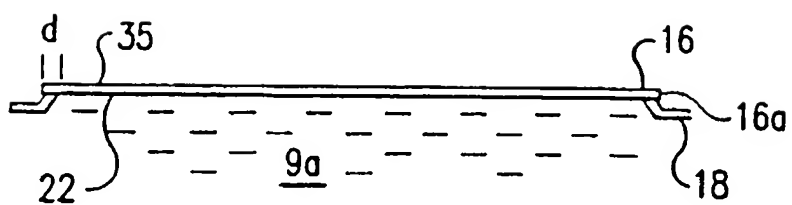


FIG. 5

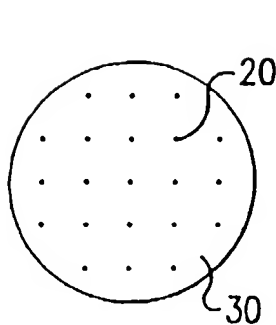


FIG. 6A

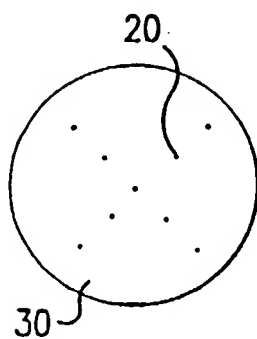


FIG. 6B

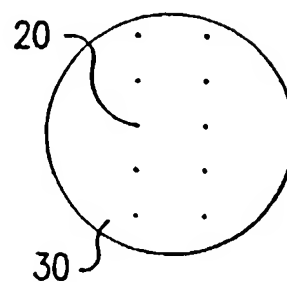


FIG. 6C

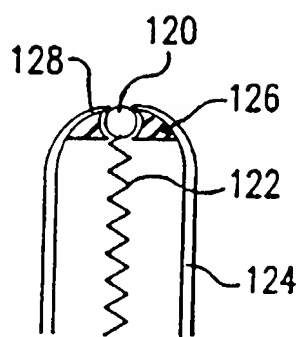


FIG. 7

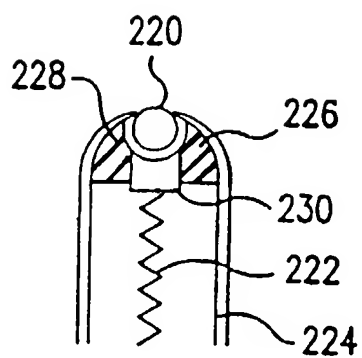


FIG. 8

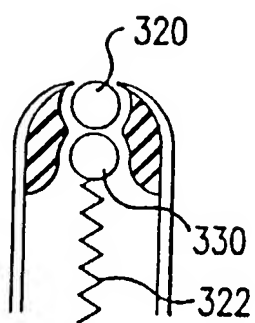


FIG. 9

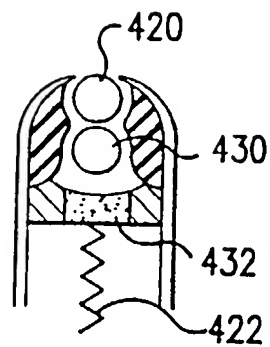
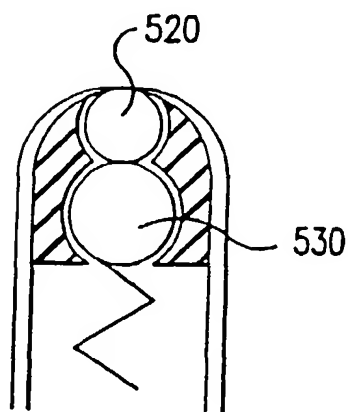
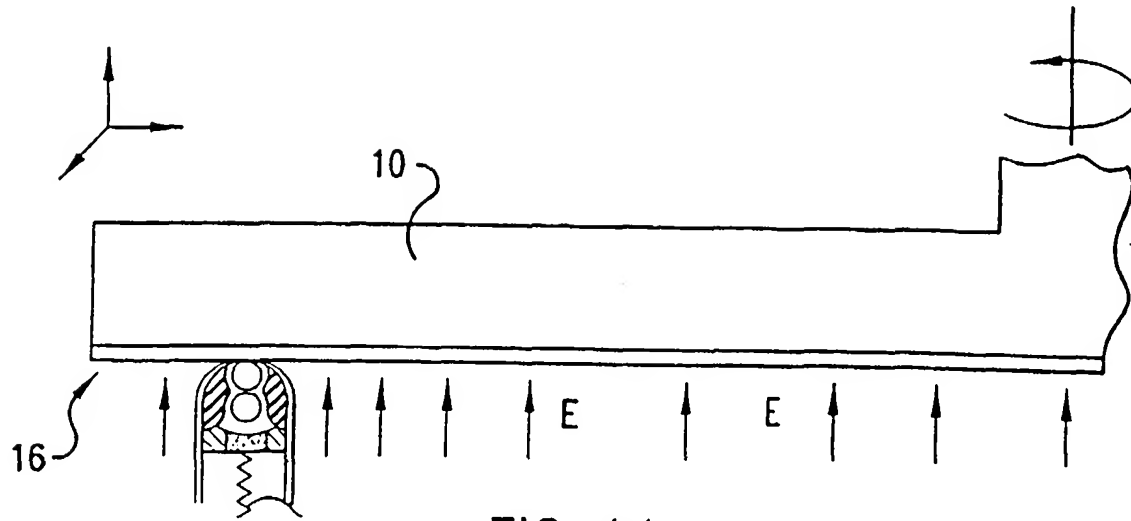


FIG. 10



# INTERNATIONAL SEARCH REPORT

International application No.

PCT/US01/05552

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : C25D 7/12, 5/52, 5/00, 5/18, 17/00; C25B 9/00  
US CL : 204/ 212, 242; 205/ 157, 222, 137, 143, 103, 105

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
U.S. : 204/ 212, 242; 205/ 157, 222, 137, 143, 103, 105

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
Please See Continuation Sheet

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X --- Y	US 5,472,592 A (LOWERY) 05 December 1995 (05.12.1995), column 1, lines 42-51 and Fig. 1	1-3,6,8-11,14-15,18-20,22 ----- 4-5,12-13,21,23-24
Y	US 6,004,880 A (LIU ET AL.) 21 December 1999 (21.12.1999), column 4, lines 9-19 and Fig. 2.	7,16,17
Y,P	US 6,176,992 B1 (TALIEH) 23 June 2001 (23.06.2001), column 3, line 66 to column 4, line 60 and Figs. 1B and 2.	7,16,17

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

* Special categories of cited documents:	
* "A" document defining the general state of the art which is not considered to be of particular relevance	* "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
* "E" earlier application or patent published on or after the international filing date	* "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
* "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	* "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
* "O" document referring to an oral disclosure, use, exhibition or other means	
* "P" document published prior to the international filing date but later than the priority date claimed	* "&" document member of the same patent family

Date of the actual completion of the international search

11 May 2001 (11.05.2001)

Date of mailing of the international search report

04 JUN 2001

Name and mailing address of the ISA/US

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Paralegal Specialist

# INTERNATIONAL SEARCH REPORT

International application No.

PCT/US01/05552

## Box I Observations where certain claims were found unsearchable (Continuation of Item 1 of first sheet)

This international report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claim Nos.:  
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claim Nos.:  
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
3. ☐ Claim Nos.:  
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

## Box II Observations where unity of invention is lacking (Continuation of Item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:  
Please See Continuation Sheet

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
  2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
  3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
  4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.: 1-24
- Remark on Protest ☐ The additional search fees were accompanied by the applicant's protest.  
☐ No protest accompanied the payment of additional search fees.



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/US01/05552

**BOX II. OBSERVATIONS WHERE UNITY OF INVENTION IS LACKING** This application contains the following inventions or groups of inventions which are not so linked as to form a single general inventive concept under PCT Rule 13.1. In order for all inventions to be examined, the appropriate additional examination fees must be paid.

Group I, claim(s) 1-24, drawn to a device by which substantially uniform deposition of conductive material on a surface of a substrate and a method of providing substantially uniform deposition of conductive material on a surface of a substrate.

Group II, claim(s) 25-31, drawn to a device by which substantially uniform deposition of conductive material on a surface of a substrate.

Group III, claim(s) 32-39, drawn to a method of providing substantially uniform deposition of conductive material on a surface of a substrate.

Group IV, claim(s) 40-41, drawn to a device by which substantially uniform electro-etching of a conductive material on surface of a substrate and a method of providing substantially uniform electro-etching of a conductive material on a surface of a substrate.

The inventions listed as Groups I-IV do not relate to a single general inventive concept under PCT Rule 13.1 because, under PCT Rule 13.2, they lack the same or corresponding special technical features for the following reasons:

Group I is a device and method that do not comprise a conductive pad through which electrolyte can flow.

Group II is a device that comprises a conductive pad through which electrolyte can flow.

Group III is a method that comprises a step of reversing a polarity of a potential.

Group IV is a device and method that electro-etches.

The devices are different because the connection of the components for electrodeposition will be different from the connection of components for etching.

The devices of Groups I and II do not comprise means for reversing a polarity of a potential which will be needed to carry out a process of Group III.

The method of Group III comprises flowing electrolyte through the conductive pad while the method of Group I does not, but the method of Group I also comprises multiple electrical contacts including electrical contacts having rollers at the tips thereof biased towards that surface whereas the method of Group III does not comprise them.

**Continuation of B. FIELDS SEARCHED** Item 3: WEST  
search terms: semiconductor wafer, contacts, pad, anode, cathode